

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10016911-2

APPLICATION NO.

CONFIRMATION NO.

APPLICANT

Peter J Fricke et al

FILING DATE

Herewith

GROUP

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A	6,521,958	Feb 2003	Forbes et al	
	1B	6,297,989	Oct 2001	Cloud et al	
	1C	6,288,437	Sep 2001	Forbes et al	
	1D	6,118,138	Sep 2000	Farnworth et al	
	1E	5,847,441	Dec 1998	Cutter et al	
	1F	6,483,736	Nov 2002	Johnson et al	
	1G	6,661,691	Dec 2003	Fricke et al	
	1H	6,051,851	Apr 2000	Ohmi et al	
	1I	6,153,468	Nov 2000	Forbes et al	
	1J	5,625,220	Apr 1997	Liu et al	
	1K	5,581,501	Dec 1996	Sansbury et al	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	1Q	Chan et al, "Multiple Layers of CMOS Integrated Circuits Using Recrystallized Silicon Film", IEEE Electron Device Letters, V.22 (2), Feb 2001, pp 77-79
	1R	Thomas H Lee, "A Vertical Leap for Microchips", Scientific American, Jan 2002, pp 53-59
	1S	Hamdy et al, "Dielectric based antifuses for logic and memory ICs", IEEE Int'l Electron Devices Meeting, IEDM 88, Augst 1988, pp 786-789

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	2A	2002/0083390	Jun 27, 2002	Lee et al	
	2B	2002/0075719	Jun 20, 2002	Johnson et al	
	2C	2002/0058408	May 16, 2002	Maydan et al	
	2D	2001/0055838	Dec 27, 2001	Walker et al	
	2E	2001/0036750	Nov 01, 2001	Radens et al	
	2F	2001/0011776	Aug 09, 2001	Igarashi et al	
	2G	6,372,633	Apr 16, 2002	Maydan et al	
	2H	6,380,003	Apr 30, 2002	Jahnes et al	
	2I	6,351,406	Feb 26, 2002	Johnson et al	
	2J	6,251,710	Jun 26, 2001	Radens et al	
	2K	6,185,122	Feb 06, 2001	Johnson et al	

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	2L					
	2M					
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	2O					
	2P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	2Q	Chenming Hu, "Interconnect devices for field programmable gate array", IEEE Int'l Electron Devices Meeting, IEDM 92, April 1992, pp 591-594
	2R	Jonathan Green et al, "Antifuse Field Programmable Gate Arrays", Proc. IEEE, Vol 81(7), July 1993, pp 1042-1056
	2S	Kulkarni et al, "Patterning of Submicron Metal Features and Pillars in Multilevel Metallization", J. Electrochem Soc., V. 135 (12), Dec. 1988, pp 3094-3098

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	3A	6,111,302	Aug 29, 2000	Zhang et al	
	3B	6,034,882	Mar 07, 2000	Johnson et al	
	3C	5,821,558	Oct 13, 1998	Han et al	
	3D	5,751,012	May 12, 1998	Wolstenholme et al	
	3E	5,335,219	Aug 02, 1994	Ovshinsky et al	
	3F	4,599,705	Jul 08, 1986	Holmberg et al	
	3G	4,499,557	Feb 12, 1985	Holmberg et al	
	3H	3,641,516	Feb 08, 1972	Castrucci et al	
	3I	3,530,441	Sep 22, 1970	Ovshinsky	
	3J	3,271,591	Sep 06, 1966	Ovshinsky	
	3K				

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